



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 016910-0476

In reissue patent application of

Nobuo FUDANUKI et al.

Serial No: 09/963,735

Application for reissue of U.S. Patent No.
6,054,872, granted April 25, 2002

Filed: September 27, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH MIXED GATE
ARRAY AND STANDARD CELL

REPLY UNDER 37 C.F.R. § 1.111

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

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MAR 25 2003
TECHNOLOGY CENTER 2800

This is a reply to the Office Action mailed on November 19, 2002, the period for reply extending to March 19, 2003 by virtue of a petition and fee for a one-month extension of time filed concurrently herewith.

Applicants request that the client/matter number for this application be changed to 016910/0476. Applicants request that the Examiner acknowledge that this change has been made.

Claims 1-65 were pending in this application. No claims have been amended, canceled or added. Accordingly, claims 1-65 are submitted for reconsideration. Applicants appreciate the allowance of claims 1-65.

In the Office Action, a new declaration was required that acknowledged the filing of any foreign application. Concurrently herewith, Applicants have provided a Supplemental Declaration acknowledging the filing of foreign applications.

The Office Action also required that the original patent be surrendered. Concurrently herewith, Applicants have surrendered the original patent.

Applicants respectfully submit that the application is in condition for allowance and request reconsideration. Should the Examiner have any questions or suggestions regarding this application, the Examiner is invited to contact the undersigned attorney at the telephone number shown below.

Respectfully submitted,

3/19/03
Date

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